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بقي	Application No.	Applicant(s)
	09/852,647	JUN, SAHNG-IK
Notice of Allowability	Examiner	Art Unit
	Thoi V. Duong	2871
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to the amendment filed March 31, 2005.		
2. The allowed claim(s) is/are <u>1-38</u> .		
3. $\boxtimes$ The drawings filed on <u>11 May 2001</u> are accepted by the Ex	aminer.	
4.		
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☒ Examiner's Amend	te

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## **DETAILED ACTION**

1. This office action is in response to the Amendment filed March 31, 2005.

Accordingly, claims 1-22, 27, 30, 32, 33 and 35-38 were amended. Currently, claims 1-38 are pending in this application.

## **EXAMINER'S AMENDMENT**

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Hae-Chan Park (Reg. No. 50,114) on 06/06/2005.

In the claim:

<u>Claim 3</u>: in line 2, after "pattern and", delete "a" and insert --the--; and after "electrode or", delete "a" and insert --the--.

## Allowable Subject Matter

Claims 1-38 are allowed.

The following is an examiner's statement of reasons for allowance: none of the prior art of record fairly suggests or shows all of the limitations as claimed. Specifically,

Re claims 1 and 35, none of the prior art of record discloses, in combination with other limitations as claimed, a liquid crystal display comprising a semiconductive layer comprising a semiconductor pattern and a light interception pattern disposed on a single plane.

Re claims 10 and 22, none of the prior art of record discloses, in combination with other limitations as claimed, a liquid crystal display (as well as a method of fabricating the same) comprising a light interception pattern formed of the same material as the semiconductor pattern on the gate insulating layer.

The most relevant references, USPN 6,466,289 B1 of Lee et al. and USPN 5,247,289 of Matsueda, fail to disclose or suggest a semiconductive layer comprising a semiconductor pattern and a light interception pattern disposed on a single plane or a light interception pattern formed of the same material as the semiconductor pattern on the gate insulating layer. As shown in Figs. 3, 6 and 7, Lee et al. discloses a light interception pattern 64 and a semiconductor pattern 40 formed on the gate insulating layer 30; however, the material of the light interception pattern 64 is different from that of the semiconductor pattern 40 (col. 3, lines 33-34 and 54-56). Meanwhile, as shown in Fig. 7, the Matsueda's reference discloses a semiconductor portion formed of the same layer as a semiconductor pattern 78 on top of a common electrode 81; however, this semiconductor portion does not function as a light interception pattern.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thoi V. Duong whose telephone number is (703) 308-

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3171. The examiner can normally be reached on Monday-Friday from 8:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim, can be reached at (703) 305-3492.

Thoi Duong

Into

06/06/2005

DUNGT. NGUYEN PRIMARY EXAMINER